

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	31694	NAND adj gate	USPA T	2004/05/1 8 14:31	
2	BRS	L2	4778	NAND adj circuit	USPA T	2004/05/1 8 14:32	
3	BRS	L3	35111	1 or 2	USPA T	2004/05/1 8 14:32	
4	BRS	L4	6596	3 and (326/\$.ccls. or 327/\$.ccls.)	USPA T	2004/05/1 8 14:34	
5	BRS	L5	7951	3 and CMOS	USPA T	2004/05/1 8 14:34	
6	BRS	L6	2772	4 and cmos	USPA T	2004/05/1 8 14:34	
7	BRS	L7	1622	6 and parallel	USPA T	2004/05/1 8 14:36	